

**Savitribai Phule Pune University**  
**Information to be published on the website**  
**Admission 2016-17**

**Annexure A**

1		Name of Department:	DESIGN INNOVATION CENTRE	
2		Courses offered:		
	a	Name of the course/s	Design and Development of Signal conditioning and Sensor Technology	
	b	Duration of course/s	Course	Duration in months
			1 Design and Development of Signal conditioning and Sensor Technology	6 months
	c	Application fee:	Course	Open Category & Outside the state of Maharashtra in Rs
			1 Design and Development of Signal conditioning and Sensor Technology	400
	d	Course Fee :	Course	Fee
			1 Design and Development of Signal conditioning and Sensor Technology (For Candidates Domicile in Maharashtra State) (For Candidates from outside Maharashtra state)	Tuition Fees- Rs 3000/- (Rs 100/- per credit)-30 credits Laboratory Fees- Rs 2000/- (Rs 2000/- per Semester) <b>Total Fees- Rs 5000/-</b>

e.	Syllabus of the course/s:	Please Visit : <a href="http://unipune.ac.in/snc/dic/pdf/design-and-development-of-signal-conditioning-and-sensor-technology_DIC_f_5.pdf">http://unipune.ac.in/snc/dic/pdf/design-and-development-of-signal-conditioning-and-sensor-technology_DIC_f_5.pdf</a>		
f .	Eligibility:	Course	Qualifying Exam and Eligibility	Minimum Percentage required
				Open      Reserved
		1 Design and Development of Signal conditioning and Sensor Technology	Masters degree in Physics, Electronics,Instrumentation	
g.	Required documents:	<ul style="list-style-type: none"> <li>• Attested copy of final-year mark sheet if you have already obtained your qualifying degree OR attested copy of your previous-year mark sheet. If you will be appearing for the final-year exam of your qualifying degree during academic year 2015-16.</li> <li>• Attested copies of caste and non-creamy-layer certificates as applicable.</li> <li>• Attested copy of domicile certificate OR certificate of SSC and HSC examinations if candidate is domicile of Maharashtra.</li> <li>• Medical Certificate in case of Physically handicapped</li> </ul>		
h.	Number of Seats available:	Course	Seats	
		1 Design and Development of Signal conditioning and Sensor Technology	20	

3.	Fellowships:	1	Nil
4.	Legal Reservations:	Category	
		SC	13%
		ST	07%
		DT(A)	03%
		NT(B)	2.5%
		NT(C)	3.5%
		NT(D)	02%
		OBC	19%
		SBC	(as per Govt. of Maharashtra rule.)
5.		Social Reservations:	3% seats are reserved for Physically handicapped Students 5% Seats are reserved for Defiance Personnel's child/Ex-Serviceman Child. 30% Seats are reserved for female Candidates for graduate Courses
		One Migrant student from Jammu & Kashmir, per course over and above the sanctioned intake capacity will be admitted as per the merit in the entrance test.	

6.		Date of Entrance Test:	Not applicable	
	a	Examination Center	Course1	Course 2
	b	Model of question Paper		<<Enter Name of Course here>>
			Nature of Questions-Objectives	Nature of Questions
			Number and names of sections in Question paper- No Sections	Number and names of sections in Question paper
			Section wise weight age of marks- Not Applicable	Section wise weight age of marks
			Number of Questions-	Number of Questions
			Marks per question- for correct answer and - for wrong answer	Marks per question
			Total Marks-	Total Marks
			Passing Criteria-	Passing Criteria
			Negative marking – Yes	Negative marking

7.	Merit List / Admission Rules:	Admission should be done as per ordinance 181.-A Circular No.68/ 2014 Dated 27/03/2014
8.	Important Date	
a	Start date of online application:	15 <sup>th</sup> July 2016
b	End date of Online application:	10 <sup>th</sup> August 2016
c	Last date of submission of application form	16 <sup>th</sup> August 2016
d	Date of Entrance Test:	Not applicable
e	Time of Entrance Test:	Not applicable
f	Date of declaration of result of the Entrance test:	Not applicable
g	Date of Group Discussion /Personal Interview.	Not applicable
h	Date of publication of merit list	24 <sup>th</sup> August 2016
i	Date of publication of successive merit list	
j	Date of admission	30 <sup>th</sup> August 2016
k	Date of Commencement of the course	5 <sup>th</sup> September 2016
	Instruction: There is possibility of changing the time & dates in the above columns ( columns 8 e to 8 k )	

9.	Department Contact Info*	<a href="http://unipune.ac.in/snc/dic/">http://unipune.ac.in/snc/dic/</a> * For any information on the nature, scope and prospects of the course, candidates should log into the website of the related department. No direct queries/ phone calls on the pattern of the question paper will be entertained.
	<b>Design and Development of Signal conditioning and Sensor Technology (This course will be conducted at two centers)</b>	
A	Name of the contact person:	<b>(Center 1)</b> Prof. A.D. Shaligram (SPPU Campus) <b>(Center 2)</b> Shri. Yogesh Kulkarni (Vigyan Ashram, Pabal)
B	Designation:	Course Coordinator
C	Contact timings:	10.30 am to 5.30 pm
d	Telephone Number/s:	<b>(Center 1)</b> 020-25699841, 6060, 6061 <b>(Center 2)</b> 9730005016
e	e.mail	<b>(Center 1)</b> <a href="mailto:webmaster@electronics.unipune.ac.in">webmaster@electronics.unipune.ac.in</a> <b>(Center 2)</b> <a href="mailto:vapabal@gmail.com">vapabal@gmail.com</a>
f	Address:	<b>(Center 1)</b> Department of Electronic sciences, Savitribai Phule Pune University, Ganesh Khind Road, Pune 411 007 <b>(Center 2)</b> Vigyan Ashram at Pabal, Vigyan Ashram, Pabal, Dist. Pune-412403 Maharashtra, India